

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: To be assigned  
Filing Date: Herewith  
Applicants: Hiroyasu Ito et al.  
Group Art Unit: To be assigned  
Examiner: To be assigned  
Title: Semiconductor Device And Method Of Manufacturing The Same  
Attorney Docket: 4041J-000437/DVA

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Director of the United States Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

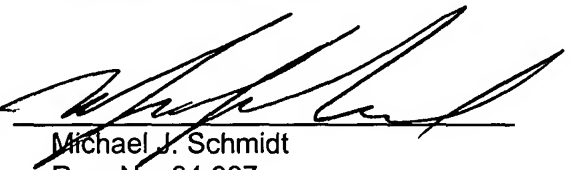
In accordance with the statutory provisions set forth in 37 CFR §1.97 and 1.98, the Examiner is hereby respectfully advised of the references listed on the attached PTO-1449 Form(s). All of these references were made of record during prosecution of the parent application, Serial No. 09/340,020 filed June 28, 1999, from which this divisional claims priority.

The listed references are cited only in the interest of candor and without any admission that they constitute statutory prior art, or contain matter which anticipates the invention or which would render the same obvious, either singly or in combination, to a person of ordinary skill in the art.

Respectfully submitted,

Dated: August 18, 2003

By

  
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<p>FORM HDP-1449 (Based on Form PTO-1449)</p> <p><b>PATENT AND TRADEMARK OFFICE</b></p> <p><b>INFORMATION DISCLOSURE CITATION</b></p> <p>(Use several sheets if necessary)</p> <p>Sheet 1 of 2</p>	ATTORNEY DOCKET NO.	SERIAL NO.
	4041J-000437/DVA	To be assigned
	APPLICANT	
	Hiroyasu Ito et al.	
	FILING DATE	GROUP
Herewith	To be assigned	

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		5,313,082	05/1994	Eklund		
2.		6,025,237	02/2000	Choi	438/301	
3.		5,304,827	04/1994	Malhi et al.	257/262	
4.		5,811,850	09/1998	Smayling et al.	257/335	
5.		6,025,231	02/2000	Hutter et al.	438/268	
6.		5,382,536	01/1995	Malhi et al.	437/41	
7.		4,750,024	06/1988	Schreck	357/23.5	
8.		5,192,872	03/1993	Lee	257/315	
9.		5,382,821	01/1995	Nakajima	257/401	
10.		5,548,147	08/1996	Mei	257/333	
11.		5,917,217	06/1999	Kitamura et al.	257/343	
12.		5,929,478	07/1999	Parris et al.	257/314	
13.		5,501,994	03/1996	Mei	437/40	
14.		5,141,880	08/1992	Inoue et al.	437/29	

FOREIGN PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes No
1.		NONE				

Examiner:	Date Considered:
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EXAMINER: Please initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	FILING DATE	GROUP
	Herewith	To be assigned

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
1.		Wolf, "Silicon Processing for the VLSI Era," 1990, p. 66-67

Examiner:	Date Considered:
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EXAMINER: Please initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.